

DOCUMENT-IDENTIFIER: US 20010000950 A1

TITLE: Method for elimination of parasitic bipolar action  
in silicon on  
insulator (SOI) dynamic logic circuits

----- KWIC -----

18. The method of claim 17 further comprising: providing a first signal to a first node of said SOI device; providing a second signal to an input of said SOI device; activating the conduction of said active discharging device according to the state of said first and second signals; and providing said logic output function according to the following definition OUT=NOT[ (A\*A1)+(A\*B1)+(B\*A1)+(B\*B1) ] where A, B, A1 and B1 are inputs to said expanded stack circuit.

19. The method of claim 18 wherein the step of providing said first signal includes providing a clock signal to said first node.

21. The method of claim 18 wherein the step of providing said second signal includes applying an active low signal to said input during a pre-charging cycle.

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18. The method of claim 17 further comprising: providing a first signal to a first node of said SOI device; providing a second signal to an input of said SOI device; activating the conduction of said active discharging device according to the state of said first and second signals; and providing said logic output function according to the following definition OUT=NOT[ (A\*A1)+(A\*B1)+(B\*A1)+(B\*B1) ] where A, B, A1 and B1 are inputs to said expanded stack circuit.

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	22018	(control or controlling or controlled) adj3 (conduction or conductive or conductor)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM_TD B	2002/09/03 15:34
2	BRS	L2	827	1 and (first adj signal) and (second adj signal)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM_TD B	2002/09/03 15:35
3	BRS	L3	146	2 and (clock adj signal)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM_TD B	2002/09/03 15:43
4	BRS	L4	12	3 and (active adj low)	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM_TD B	2002/09/03 15:46
5	BRS	L5	1	3 and SOI	USPAT; US-PGP UB; EPO; JPO; DERWENT; IBM_TD B	2002/09/03 15:48

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	335	(activate or activating or activated) adj3 conduction	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/09/03 14:09
2	BRS	L2	13	1 and (first adj signal) and (second adj signal)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/09/03 14:11
3	BRS	L3	2	2 and ((control or controlling or controlled) adj3 conduction)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/09/03 14:12